


# PGT200 - Operating Systems

This course is *Operating Systems*, offered by the Department of Electronics Engineering Technology.

## Announcement

[20161007] Just updated  Some info are still from the previous offering...

## Lecture Slides

- Lecture 0 - [Course Introduction](#)

These are from Tanenbaum's Book:

- Chapter 1 - [Introduction](#)
- Chapter 2 - [Processes and Threads](#)
- Chapter 3 - [Memory management](#)
- Chapter 4 - [File systems](#)
- Chapter 5 - [Input/Output](#)

## Lab Notes

- Lab Work 1 - [Tools and Platform](#)
- Lab Work 2 - [Processes and Threads \(Part 1,Part 2\)](#)
- Lab Work 3 - [Bootstrap Loader](#)
- Lab Work 4 - [Simple Kernel](#)

## Useful Links

- [OSDev Wiki](#)
- [OSDever Tutorials](#)
- [Custom Bootloader](#) - OSDev Wiki
- [Higher Half Kernel](#) - OSDev Wiki

## Assignments

- Assignment 1:

[pgt200\\_201617s1\\_assign1.txt](#)

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ASSIGNMENT 1  
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### The Dining Philosopher's Problem

You are to implement a solution for the dining philosophers problem using any method you see fit. The number of philosophers is fixed at 5 and each must run on its own separate process. Based on your proposed solution,

- a) explain your implementation of the chosen algorithm. [10 marks]
- b) argue which algorithm would be the best fit for this problem. [10 marks]
- c) answer general questions [5 marks]

This is an individual assignment. An example framework has been prepared for you to refer to.

ASSIGNMENT DUE: W07/14 17-21/10/2016 (Anytime during the week)

You can use [this](#) as a reference.

- Assignment 2:

[pgt200\\_201617s1\\_assign2.txt](#)

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ASSIGNMENT 2  
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Evaluate any two existing filesystems in desktop OS (e.g. Linux, BSD, Unix, Windows, MacOS). Based on the given sub-topics, identify and evaluate their respective implementations.

- a) General filesystem layout
- b) File creation/deletion
- c) Other features

This is an individual assignment.

ASSIGNMENT DUE: W12/14 {28/11-2/12}/2016 (Anytime during the week)

## Lab Project

- The following project titles uses [myload86](#) and mylplayos projects as a reference.

[pgt200\\_201617s1\\_projects.txt](#)

```
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LAB PROJECT TITLE 1  
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```

```
Based on myload86 project, write an x86 boot code (to replace  
loader.asm)  
that is capable of loading a second stage boot code LDPLAY0S.SYS  
from a file  
system other than FAT. Linux EXT file system is recommended but  
others can be  
proposed as well.
```

```
-----  
LAB PROJECT TITLE 2  
-----
```

```
Based on mylplayos project, implement any OS tasks (e.g. process  
management,  
memory management,filesystem) that have been covered in this  
course.
```

```
-----  
ASSESSMENT REQUIREMENTS  
-----
```

```
This is a group assignment - but, marks will be evaluated  
individually.  
Evaluation will be based on a demonstration and a simple report  
(not MORE  
than 10 pages) explaining your work.
```

```
PROJECT DUE: W14/14 12-16/12/2016 (Anytime of the week)
```

## Course Synopsis

This course presents the fundamental concepts and design of operating systems. Students will be guided to explore basic operating system concepts like processes, memory management, scheduling, file systems and IO management. Basic design and implementation of some of these concepts will be covered during laboratory sessions.

## Course Outcome

1. Able to discuss basic operating systems concepts
2. Able to implement basic operating systems concepts
3. Able to evaluate implementations of basic operating systems concepts

## Course Assessment

|                           | Examinations          |                    | Course Work |                |             |
|---------------------------|-----------------------|--------------------|-------------|----------------|-------------|
| <b>Total Contribution</b> | 60%                   |                    | 40%         |                |             |
| <b>Assessment</b>         | Mid-Term Examinations | Final Examinations | Assignments | Lab Assessment | Lab Project |
| <b>Contribution</b>       | 20%                   | 40%                | 10%         | 20%            | 10%         |

## Course Syllabus

| Week           | Lecture  | Laboratory         | Notes                     |
|----------------|--|--------------------|---------------------------|
| <b>Week 01</b> | <ul style="list-style-type: none"> <li>• Introduction to Operating Systems               <ul style="list-style-type: none"> <li>◦ fundamental concepts</li> <li>◦ implementations</li> </ul> </li> </ul>   | Lab Work 1         |                           |
| <b>Week 02</b> | <ul style="list-style-type: none"> <li>• Processes &amp; threads               <ul style="list-style-type: none"> <li>◦ processes - concept and implementation</li> <li>◦ threads - concept and implementation</li> <li>◦ execution and scheduling</li> </ul> </li> </ul>  | Lab Work 1 (cont.) |                           |
| <b>Week 03</b> | <ul style="list-style-type: none"> <li>• Processes &amp; threads (cont.)               <ul style="list-style-type: none"> <li>◦ critical region, mutual exclusion</li> <li>◦ implementing critical region</li> <li>◦ solutions for mutual exclusion</li> </ul> </li> </ul> | Lab Work 2         |                           |
| <b>Week 04</b> | <ul style="list-style-type: none"> <li>• Memory Management               <ul style="list-style-type: none"> <li>◦ address space &amp; memory abstraction</li> <li>◦ virtual memory (paging)</li> </ul> </li> </ul>   | Lab Work 2 (cont.) |                           |
| <b>Week 05</b> | <ul style="list-style-type: none"> <li>• Memory Management (cont.)               <ul style="list-style-type: none"> <li>◦ page replacement algorithm</li> <li>◦ segmentation (implementation and issues)</li> </ul> </li> </ul>  | Lab Work 2 (cont.) | <b>Assignment 1 Queue</b> |

| Week           | Lecture  | Laboratory          | Notes  |
|----------------|--|---------------------|--|
| <b>Week 06</b> | <ul style="list-style-type: none"> <li>• Bootstrapping <ul style="list-style-type: none"> <li>◦ bootstrapping concepts</li> <li>◦ x86 PC boot sequence</li> <li>◦ x86 PC programming</li> </ul> </li> </ul>  | Lab Work 3          | <b>Lab Assessment 1 (10%)</b>                            |
| <b>Week 07</b> | <ul style="list-style-type: none"> <li>• Bootstrapping (cont.) <ul style="list-style-type: none"> <li>◦ x86 PC programming (cont.)</li> </ul> </li> </ul>  | Lab Work 3 (cont.)  | <b>Assignment 1 Due (5%)</b>                             |
| <b>Week 08</b> | <ul style="list-style-type: none"> <li>• Bootstrapping (cont.) <ul style="list-style-type: none"> <li>◦ x86 PC programming (cont.)</li> </ul> </li> </ul>  | Lab Work 3 (cont.)  | <b>Mid-term Examination (20%)<br/>Assignment 2 Queue</b> |
| <b>Week 09</b> | <ul style="list-style-type: none"> <li>• Filesystem Management <ul style="list-style-type: none"> <li>◦ files and directories</li> <li>◦ implementation</li> </ul> </li> </ul>   | Lab Work 4          |  |
| <b>Week 10</b> | <ul style="list-style-type: none"> <li>• Filesystem Management (cont.) <ul style="list-style-type: none"> <li>◦ management and optimization</li> </ul> </li> </ul>   | Lab Work 4 (cont.)  | <b>Assignment 2 Due (5%)</b>                             |
| <b>Week 11</b> | <ul style="list-style-type: none"> <li>• I/O Management</li> <li>• devices and controllers</li> <li>• device drivers</li> </ul>  | Lab Project         | <b>Lab Assessment 2 (10%)</b>                            |
| <b>Week 12</b> | <ul style="list-style-type: none"> <li>• I/O Management (cont.) <ul style="list-style-type: none"> <li>◦ DMA, memory-mapped I/O</li> <li>◦ programmed and interrupt-driven I/O</li> </ul> </li> </ul>  | Lab Project (cont.) |  |
| <b>Week 13</b> | <ul style="list-style-type: none"> <li>• Operating System Design <ul style="list-style-type: none"> <li>◦ interface design</li> <li>◦ implementation</li> <li>◦ management trends</li> </ul> </li> </ul>   | Lab Project (cont.) |  |
| <b>Week 14</b> | <ul style="list-style-type: none"> <li>• Modern Operating Systems <ul style="list-style-type: none"> <li>◦ general overview of currently used OS <ul style="list-style-type: none"> <li>■ Unix, Linux, BSD, Windows, MacOSX</li> </ul> </li> </ul> </li> </ul> | Lab Project (cont.) | <b>Lab Project (10%)</b>                                 |

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Last update: **2020/09/13 17:17**

