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#### **Contact Information**

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### **Education**

- M.Sc. USM (2002)
  - M.Sc. by research. Thesis submitted in January 2001.
  - Thesis: Implementation of a Cascadable MLP Neural Processor with Sliding Feeder Technique
- B.Eng. (Hons.) USM (1997)
  - Specialized in Microelectronics (Integrated Circuit Design).
  - **Final Year Project**: Design of an Integrated Circuit Implementing the Discrete Wavelet Transform
  - Selected Courses: Integrated Circuit Design, Microprocessor Systems, Software Engineering.

# Skills/Knowledge

- Embedded Systems Development
  - Microprocessor/microcontroller board design, Embedded Linux (buildroot), 8051
    Assembly, Verilog/VHDL for FPGA Synthesis
- Computer Programming
  - ∘ C/C++, Java, Python, Linux (Bash) Shell scripts, Windows batch file, TCL Shell scripts
- Microelectronic Design
  - Transistor-level design of CMOS/BiCMOS integrated circuits, full custom layout editing of integrated circuits

# **Work Experience**

- Universiti Malaysia Perlis (UniMAP formerly known as KUKUM)
  - lob: Lecturer
  - o Duration: February 1, 2007 Current
- Kolej Universiti Kejuruteraan Utara Malaysia (KUKUM)
  - ∘ lob: Lecturer
  - Duration: May 15, 2002 January 31, 2007
- Sapura Technologies Sdn. Bhd.
  - Job: Systems Engineer
  - Duration: January 1, 2002 May 31, 2002
- SiRES Labs Sdn. Bhd.
  - Job: Chief Design Engineer

- o Duration: February 2, 2001 September 31, 2001
- Intel Technology Sdn. Bhd.
  - Job: Intern in the Design Automation Group
  - Duration: March 1, 2000 May 31, 2000
- Universiti Teknologi Petronas (UTP)
  - Job: Demonstrator (C++ Programming)
  - o Duration: June 1998 August 1998
- Universiti Sains Malaysia (USM)
  - Job: Research Personnel (IC Design & Testing)
    - Duration: June 2000 November 2000
    - Duration: September 1998 December 1998
    - Duration: July 1997 September 1997
    - Duration: January 1996 June 1996
  - ∘ Job: Tutor (C/C++ Programming)
    - Duration: January 1999 April 1999
    - Duration: July 1996 October 1996

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