

# MY1CV: Education

## Doctor of Philosophy (2024)

*Ph.D. at [UniMAP](#). Thesis submitted for evaluation in September 2023.*

## Master of Science (2002)

*M.Sc. by research at [School of Electrical & Electronics Engineering, USM](#), specializing in Microelectronics (Integrated Circuit Design). Thesis submitted in January 2001.*

## Bachelor of Engineering (1997)

*B.Eng. (Hons.) at [School of Electrical & Electronics Engineering, USM](#), specializing in Microelectronics (Integrated Circuit Design).*

**Final Year Project:** Title: "Design of an Integrated Circuit Implementing the Discrete Wavelet Transform". A system-level design mainly as a proof of concept. Sub-circuit simulations of the arithmetic unit (transistor-level) was done using pspice (MicroSim).

**Selected Courses:** Integrated Circuit Design, Microprocessor Systems, Software Engineering.

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