

NMK20603

Computer Architecture

Lab Session Briefing

Some background

- What it is supposed to be
⇒ Y4 Electronics Course
- What it is currently
⇒ Y2 Electronics Course
- Consequences
⇒ Cannot REALLY do CA stuff!

Expected Competency

- ▶ Digital Electronics (Circuits)
- ▶ Microprocessor Architecture
- ▶ Basic Coding

Lab Grade Contributions

- ✓ Lab Assessment 1 (10%) {1H-I}
- ✓ Lab Assessment 2 (10%) {1H-I}
- ✓ Lab Project (10%) {4W-G}

Lab Contents

- ❄️ Introduction to CAD Tools and HDL (2w)
- ❄️ Describing Combinational Logic (4w)
- ❄️ Describing Sequential Logic (2w)
- ❄️ Describing State Machine (2w)
- ❄️ Describing Simple Digital Systems (4w)

Friendly Reminder(s)

MY assessments

⇒ open book, hdl coding

⇒ no need to memorize

MY slides/notes

⇒ reference for self-practice

Practice!

Practice!

Have fun!